400

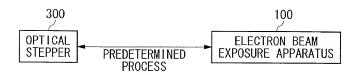


FIG. 1A

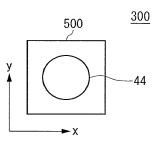


FIG. 1B

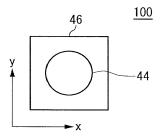
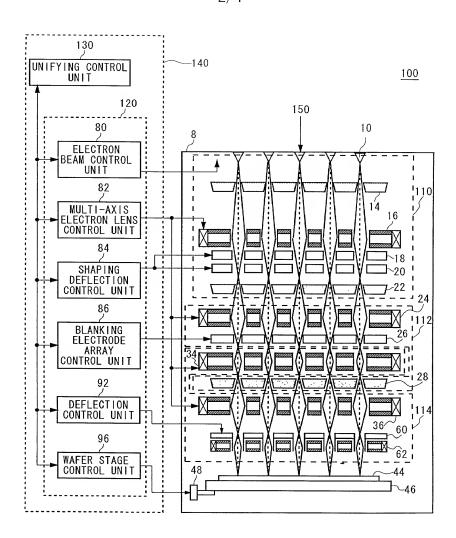
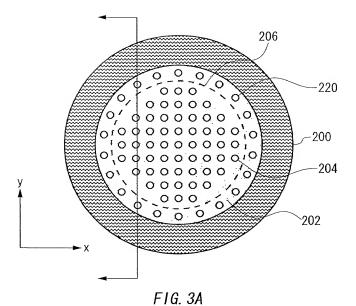


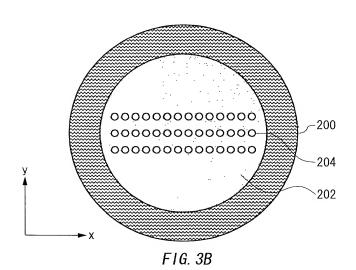
FIG. 1C



F1G. 2

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Inventor: Hiroshi YASUDA, et al. Application No.: Not yet assigned Docket No.: 514802001100 4/4 200 <u>16</u> 202 212 214 210 220 204 ′210b /210a FIG. 4A <u>16</u> 202 210 220 204 208 210b 210a FIG. 4B <u>16</u> 202 212 210 204 220 208 210b /210a

FIG. 4C

Title: Semiconductor Device Manufacturing System